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To:

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| U.S. Patent and Trademark Office Attention: Office of Initial Patent Examination | (703) 746-9195 | |

FROM: Robert E. Scheid (Reg. No. 42,126)

DATE: February 9, 2004

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| Number of pages with cover page: | 8 |
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Contents of this Transmission:

| | | |
|---|-------------------------------|------------------------------|
| Inventor: David LEWIS et al. | | Atty Docket No.:306812005300 |
| Application No.: 10/693,576 | Filing Date: October 23, 2003 | Group Art Unit: 2819 |
| Title: ARITHMETIC STRUCTURES FOR PROGRAMMABLE LOGIC DEVICES | | |
| Documents Filed: | | |
| Transmittal (1 page) | | |
| Request For Corrected Filing Receipt (2 pages) | | |
| Copy of Marked-Up Filing Receipt (2 pages) | | |
| Copy of Request for Non-Publication (1 page) | | |
| Copy of Return Receipt Postcard (1 page) | | |
| Facsimile Return Receipt Cover | | |
| Sender's Initials: | RES4/jxh3 | Date Faxed: February 9, 2004 |

PTO/SB/21 (05-03)

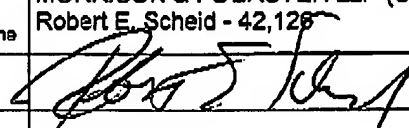
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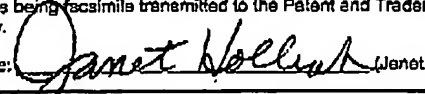
U.S. Patent and Trademark Office, U.S. DEPARTMENT OF COMMERCE

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| | | | |
|--|----------------------|------------------------|--------------|
| TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i> | Application Number | 10/693,576 | |
| | Filing Date | October 23, 2003 | |
| | First Named Inventor | David LEWIS | |
| | Art Unit | 2819 | |
| | Examiner Name | Not Yet Assigned | |
| Total Number of Pages in This Submission | 7 | Attorney Docket Number | 306812005300 |

| ENCLOSURES (Check all that apply) | | |
|---|--|--|
| <input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment/Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53 | <input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD. Number of CD(s) _____ | <input type="checkbox"/> After Allowance Communication to Group <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): Request for Corrected Filing Receipt (2 pages) Copy of Marked Filing Receipt (2 pages) Copy of Request for Non-Publication (1 page) Copy of Return Receipt Postcard (1 page) |
| Remarks: _____ | | |

| SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT | |
|--|---|
| Firm or Individual name | MORRISON & FOERSTER LLP (Customer No. 20872) Robert E. Scheid - 42,126 |
| Signature |  |
| Date | February 9, 2004 |

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| Dated: February 9, 2004 | Signature:  (Janet Hollrah) |

sf-1640679

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Dated: February 9, 2004

Signature: 

(Janet Hollrah)

Docket No.: 306812005300
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
David LEWIS et al.

Application No.: 10/693,576

Confirmation No.: 1999

Filed: October 23, 2003

Art Unit: 2819

For: ARITHMETIC STRUCTURES FOR
PROGRAMMABLE LOGIC DEVICES

Examiner: Not Yet Assigned

REQUEST FOR CORRECTED FILING RECEIPT

Filing Receipt Corrections
Office of Initial Patent Examination
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Applicant hereby requests that a corrected Filing Receipt be issued in the above-identified patent application. The official Filing Receipt received by Applicant, a copy of which is attached hereto, has an error in the filed entitled "Non-Publication Request: No".

Delete "No" and enter --Yes-- under Non-Publication Request. To support this correction, copies of the Non-Publication Request and the Return Receipt Postcard are attached to this Request.

SF-1640680

Application No.: 10/693,576

2

Docket No.: 306812005300

Applicant additionally requests that all pertinent U.S. Patent and Trademark Office records relating to the subject application be changed to reflect this correction.

Dated: February 9, 2004

Respectfully submitted,

By 

Robert E. Scheid

Registration No.: 42,126

MORRISON & FOERSTER LLP

425 Market Street

San Francisco, California 94105

(415) 268-6369

sf-1640680



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
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| APPL NO. | FILING OR 371 (c) DATE | ART UNIT | FIL FEE REC'D | ATTY. DOCKET NO | DRAWINGS | TOT CLMS | IND CLMS |
|------------|---------------------------|----------|---------------|-----------------|----------|----------|----------|
| 10/693,576 | 10/23/2003 | 2819 | 1848 | 306812005300 | 19 | 56 | 8 |

CONFIRMATION NO. 1999

20872
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MORRISON & FOERSTER

FILING RECEIPT



OC000000011763041

Date Mailed: 01/28/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

David Lewis, Toronto, CANADA;
Bruce Pedersen, Sunnyvale, CA;
Sinan Kaptanoglu, Belmont, CA;

Assignment For Published Patent Application

Altera Corporation, San Jose, CA;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 01/24/2004

Projected Publication Date: 04/28/2005

Non-Publication Request: ~~No~~ -- Yes --

Early Publication Request: No

Title

Arithmetic structures for programmable logic devices

Preliminary Class

326

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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PTO/SB/35 (08-03)

Approved for use through 07/31/2006. OMB 0551-0031

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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|---|----------------------|---|
| NONPUBLICATION REQUEST UNDER 35 U.S.C. 122(b)(2)(B)(i) | First Named Inventor | David LEWIS |
| | Title | ARITHMETIC STRUCTURES FOR PROGRAMMABLE LOGIC DEVICES |
| | Attorney Docket No. | 306812005300 |

I hereby certify that the invention disclosed in the attached application has not and will not be the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing.

I hereby request that the attached application not be published under 35 U.S.C. 122(b).

October 23, 2003

Date



Signature

(415) 268-6369

Telephone Number

Robert E. Scheid - Reg. No. 42,126

Typed or printed name

This request must be signed in compliance with 37 CFR 1.33(b) and submitted with the application upon filing.

Applicant may rescind this nonpublication request at any time. If applicant rescinds a request that an application not be published under 35 U.S.C. 122(b), the application will be scheduled for publication at eighteen months from the earliest claimed filing date for which a benefit is claimed.

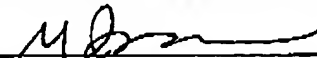
If applicant subsequently files an application directed to the invention disclosed in the attached application in another country, or under a multilateral international agreement, that requires publication of applications eighteen months after filing, the applicant must notify the United States Patent and Trademark Office of such filing within forty-five (45) days after the date of the filing of such foreign or international application. Failure to do so will result in abandonment of this application (35 U.S.C. 122(b)(2)(B)(iii)).

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as Express Mail, Airbill No. EV 302 340 314 US, in an envelope addressed to: MS Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below.

Dated:

10-23-03

Signature:



(Yayoi Isaacson)

Inventors: David LEWIS, et al.

Atty Docket No.: 306812005300

Inventors: David LEWIS, et al.

Atty Docket No.: 306812005300 ✓

Application No.: Not Yet Assigned

Filing Date: Concurrently Herewith

Title: ARITHMETIC STRUCTURES FOR PROGRAMMABLE LOGIC DEVICES

Documents Filed:

| | |
|---|---|
| Utility Patent Application Transmittal (1 page) | 37 C.F.R. 3.73(b) Statement, with copy of Assignments (5 pages) |
| Fee Transmittal, in duplicate (2 pages) | Power of Attorney (3 pages) |
| Application Data Sheet (3 pages) | Information Disclosure Statement with Form PTO-1449 (6 pages) |
| Specification (34 pages) | Copies of 13 Information Disclosure Citations |
| 19 drawings (19 sheets) | Nonpublication Request (1 page) |
| Oath or declaration, signed in counterparts (4 pages) | |
| Assignment papers (6 pages) | |

01008 U.S. PTO
10/693576

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Via: Express Mail: Airbill No. EV 302 340 314 US

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Date: October 23, 2003

sf-1587812

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